

**ABSTRACT**

A liquid crystal display having an applied horizontal electric field comprising: a gate line; a common line substantially parallel to the gate line; a data line arranged to cross the gate line and the common line to define a pixel area; a thin film transistor formed at each crossing of the gate line and the data line; a common electrode formed in the pixel area and connected to the common line; a pixel electrode connected to the thin film transistor, wherein the horizontal electric field is formed between the pixel electrode and the common electrode in the pixel area; a gate pad formed with at least one conductive layer included in the gate line; a data pad formed with at least one conductive layer included in the data line; a common pad formed with at least one conductive layer included in the common line; a passivation film to expose at least one of the gate pad, the data pad and the common pad; and a driving integrated circuit mounted on a substrate to connect directly to one of the gate pad and the data pad.